

FIG. 1

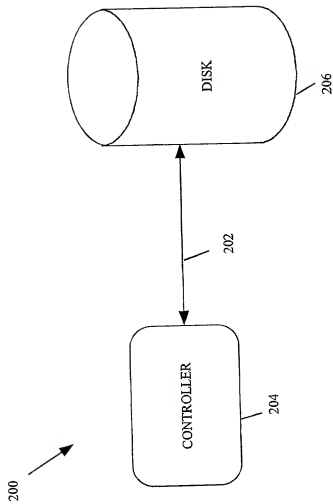


FIG. 2

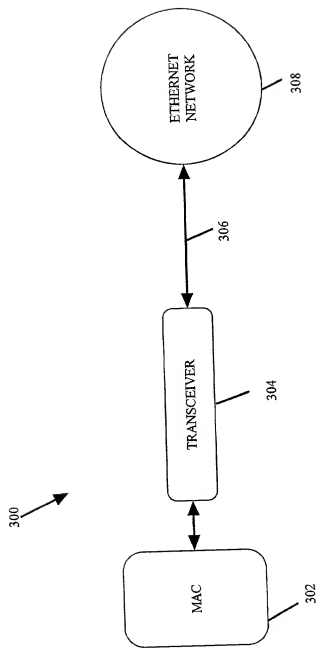
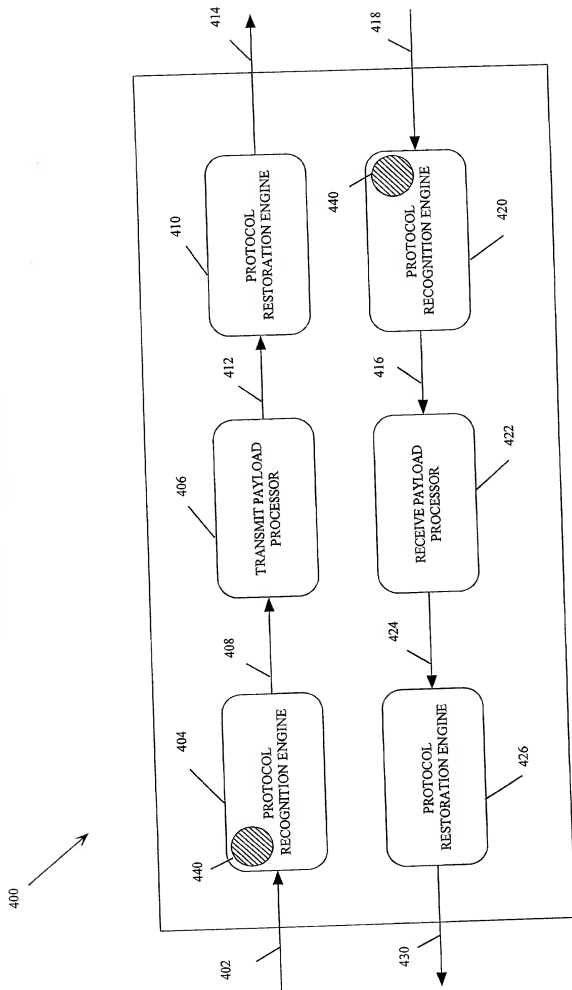


FIG. 3



**FIG. 4**

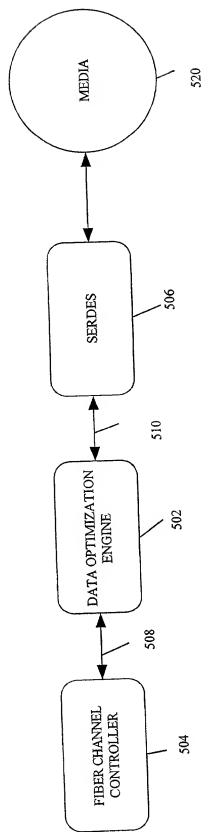
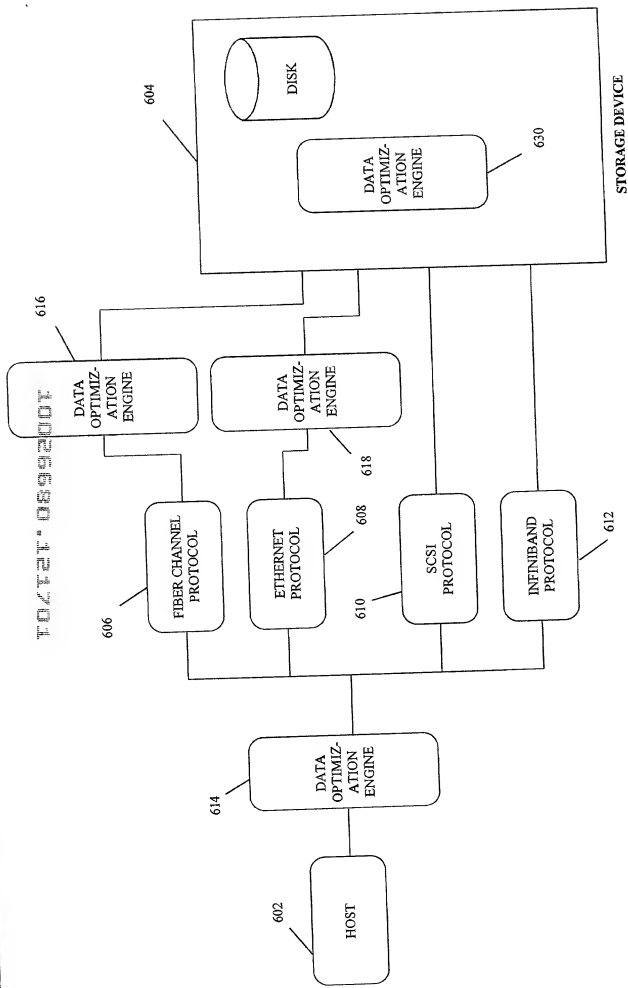
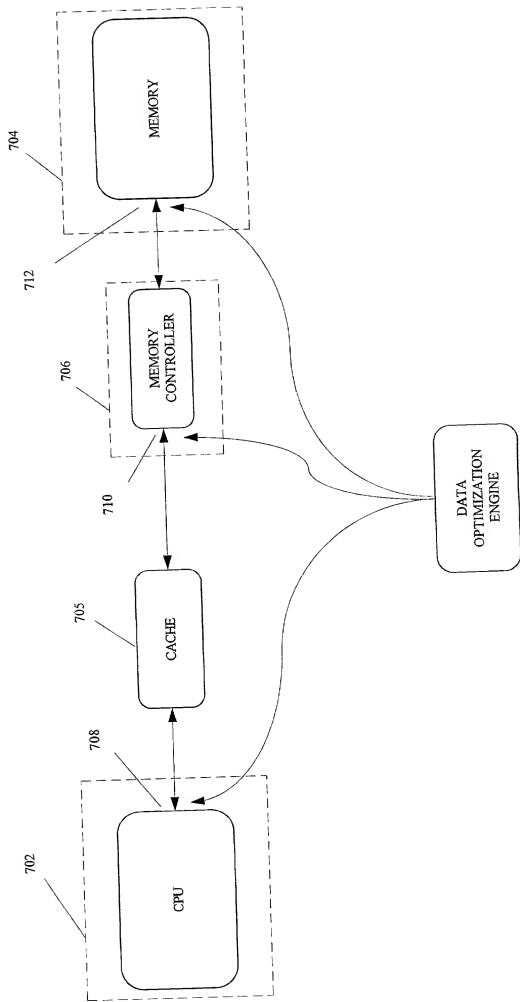


FIG. 5



**FIG. 6**

**FIG. 7**



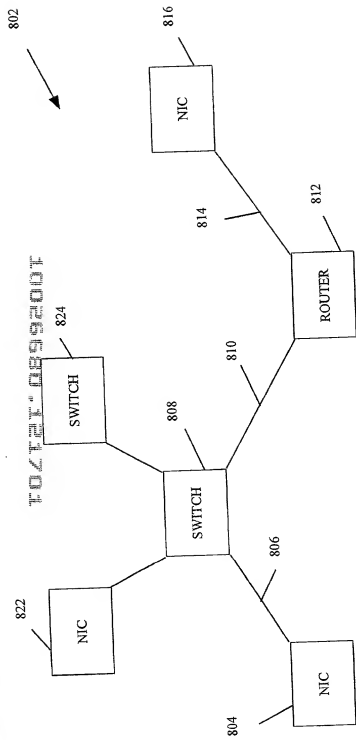


FIG. 8

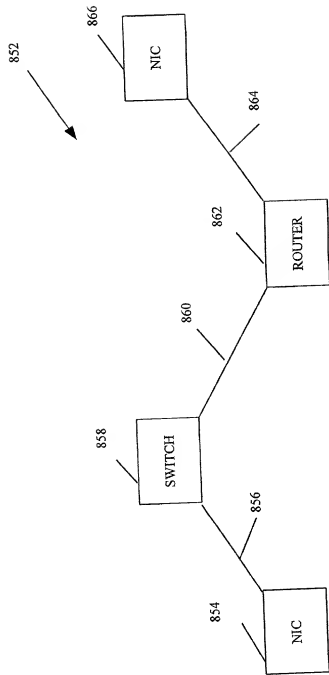
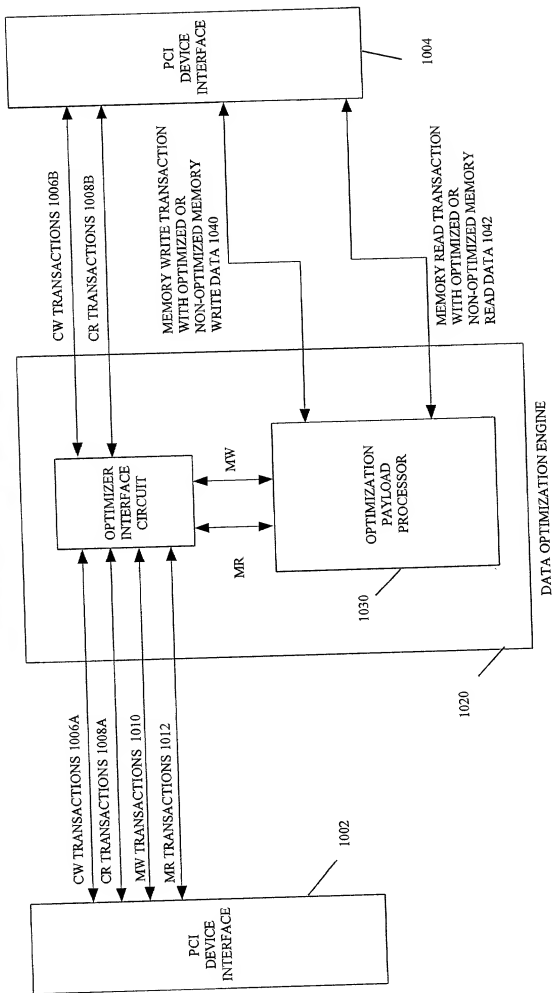


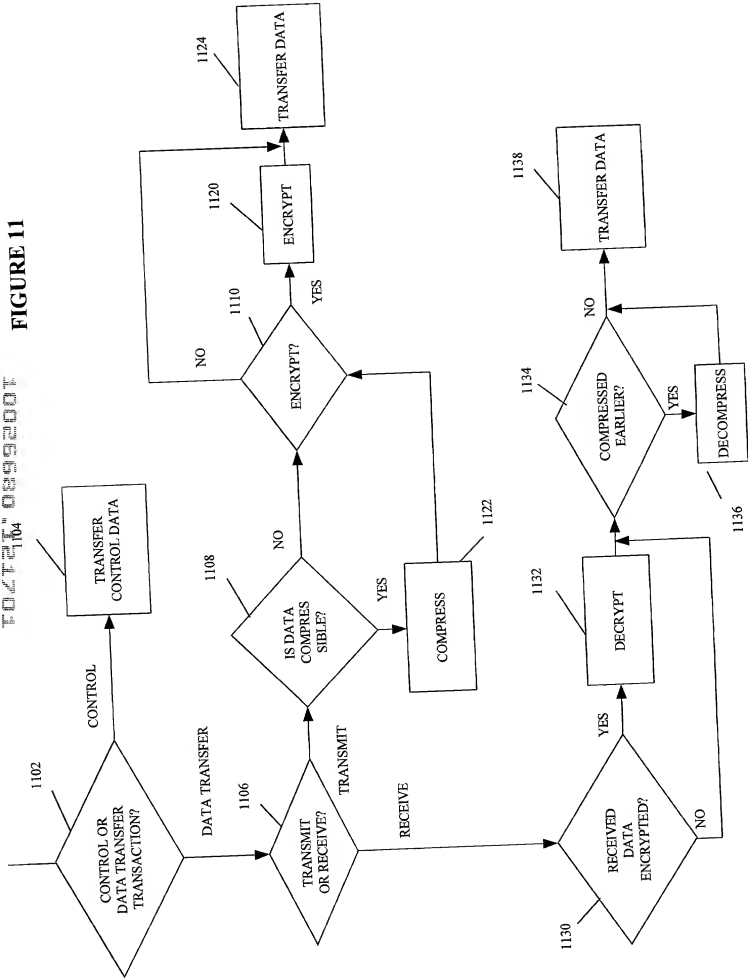
FIG. 9

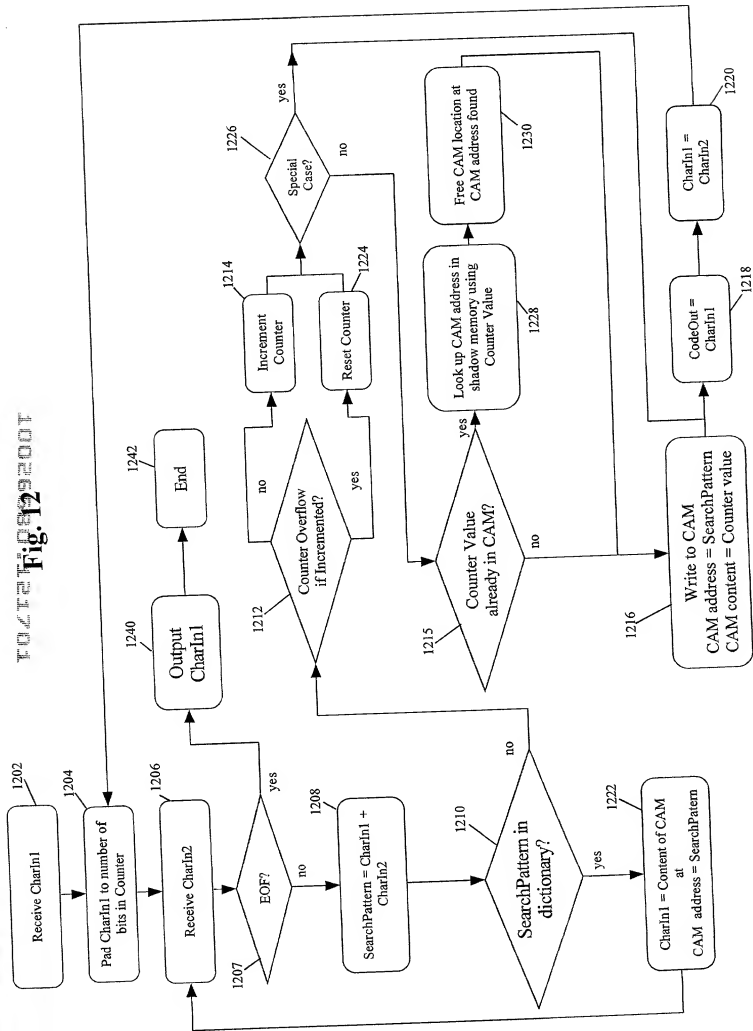




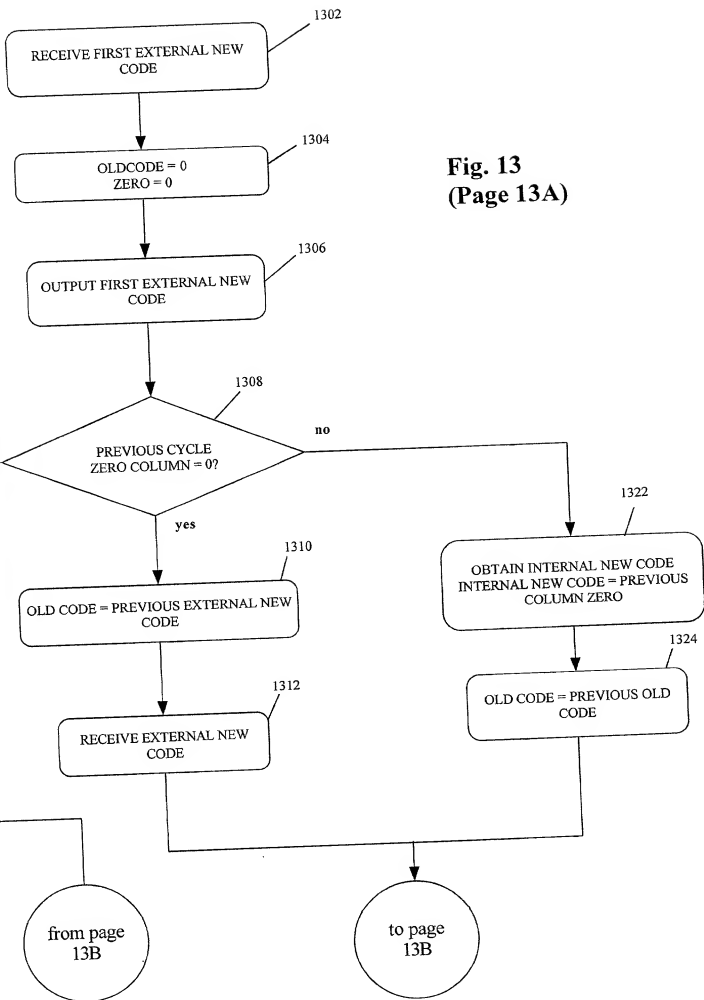
**FIG. 10**

FIGURE 11





10026580.121701



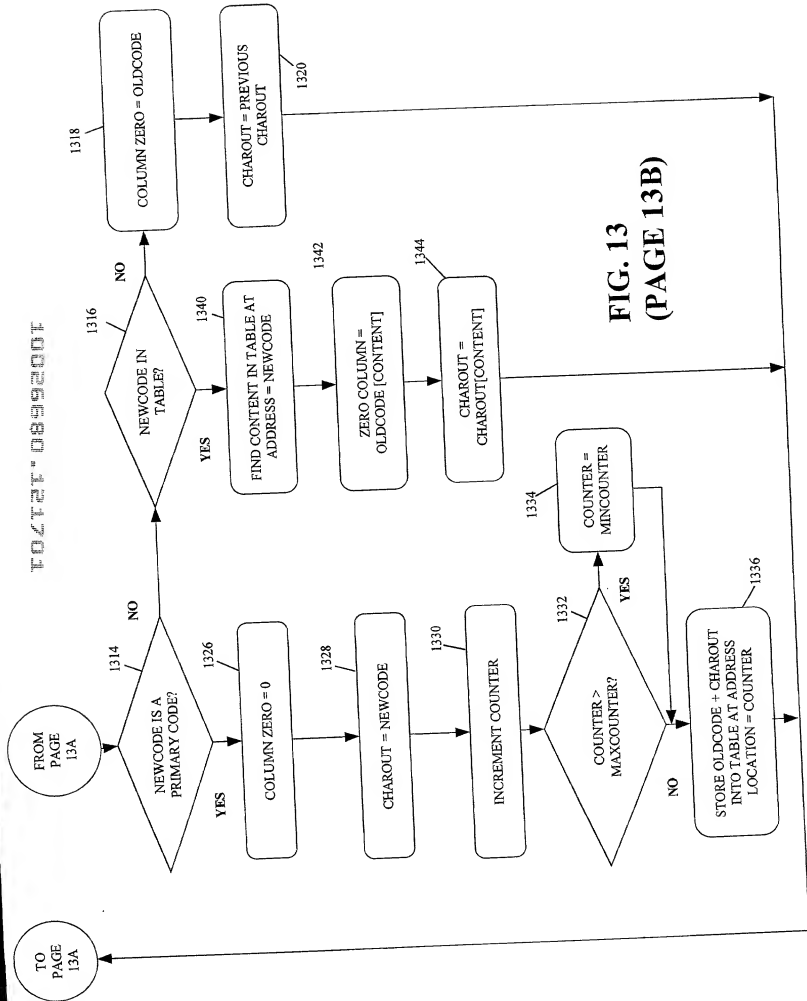
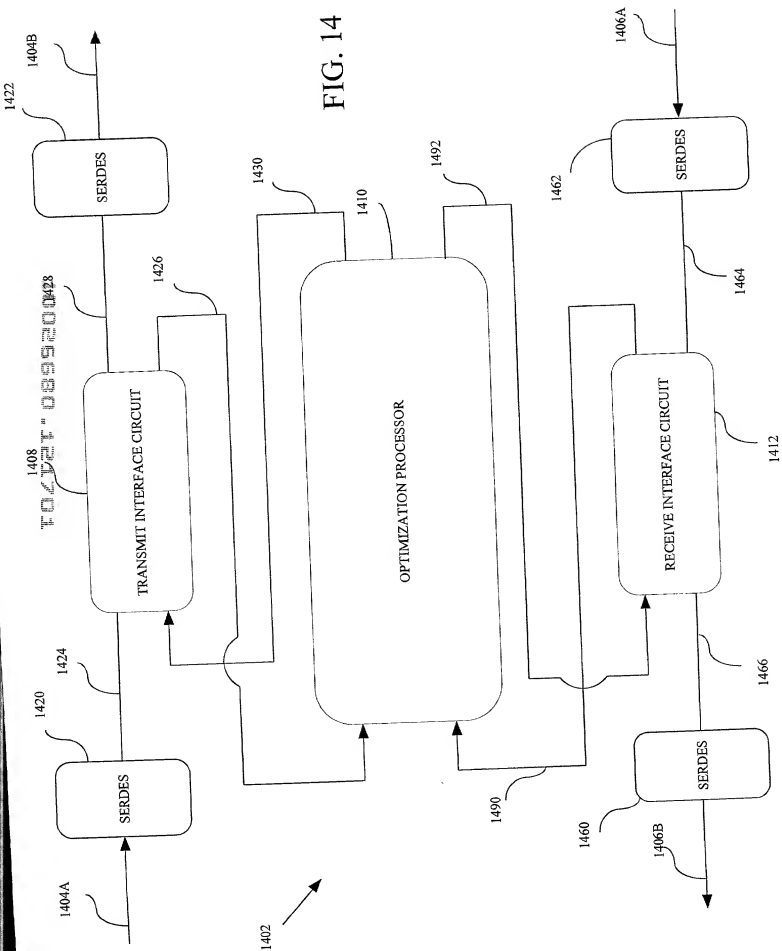


FIG. 14



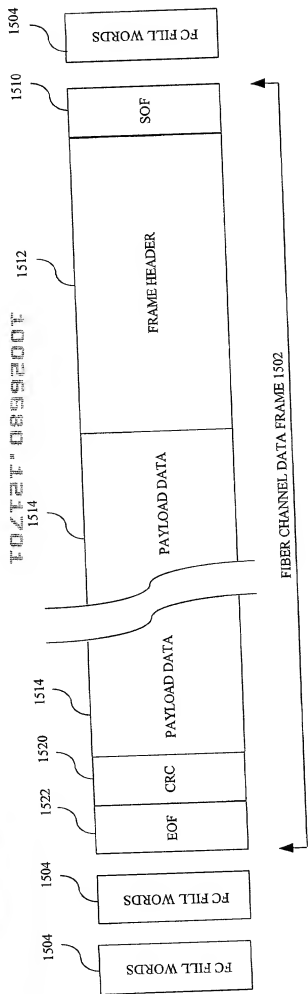


FIG. 15

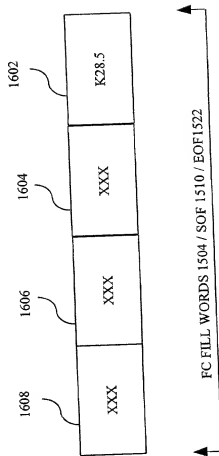


FIG. 16





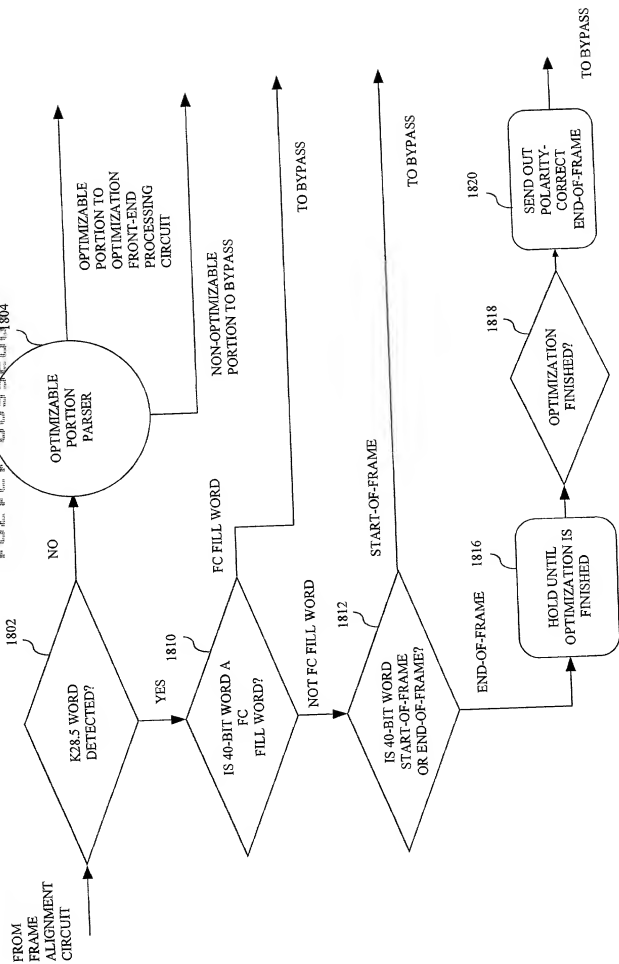


FIG. 18

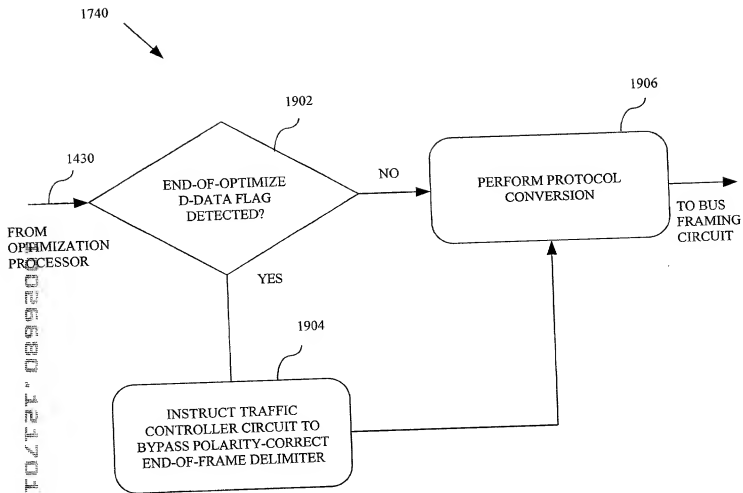


FIG. 19

FROM END-OF-OPTIMIZED-DATA  
FLAG HANDLER CIRCUIT 1740

2010

RECEIVE 8-BIT WORDS

2012

PREVIOUS 10-BIT  
WORD OUTPUT  
IS CRD+ OR CRD-  
OR NEUTRAL?

2020

USE CRD- TABLE

2014

USE CRD+ TABLE

CRD-

CRD+

NEUTRAL

2022

USE SAME TABLE AS LAST TIME

2016

OBTAIN 11-BIT CODE

2018

STRIP FLAG BIT

2024

OUTPUT 10-BIT CODE

TO BUS FRAMING  
CIRCUIT 1762

FIG. 20

10026680-121701

10026680-121701

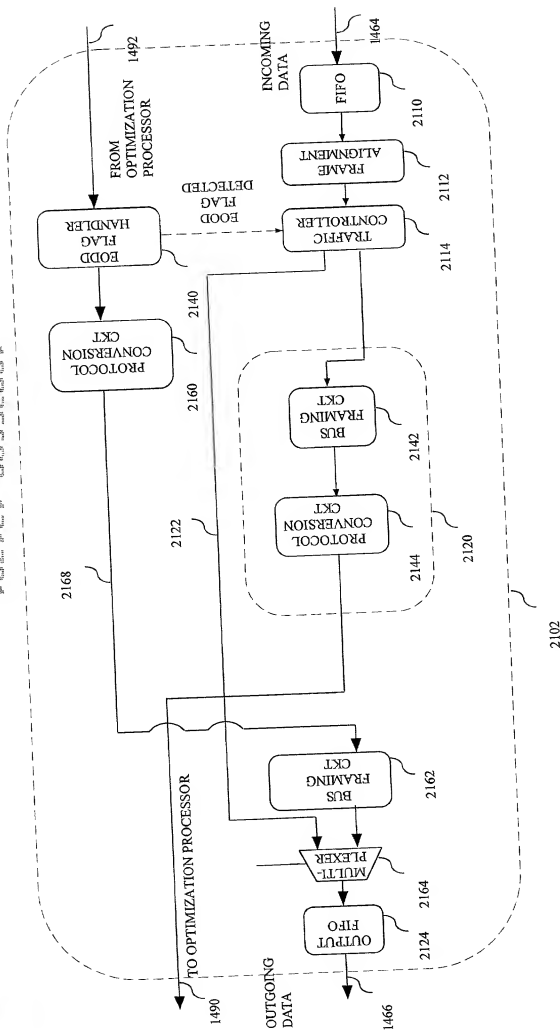


FIG. 21